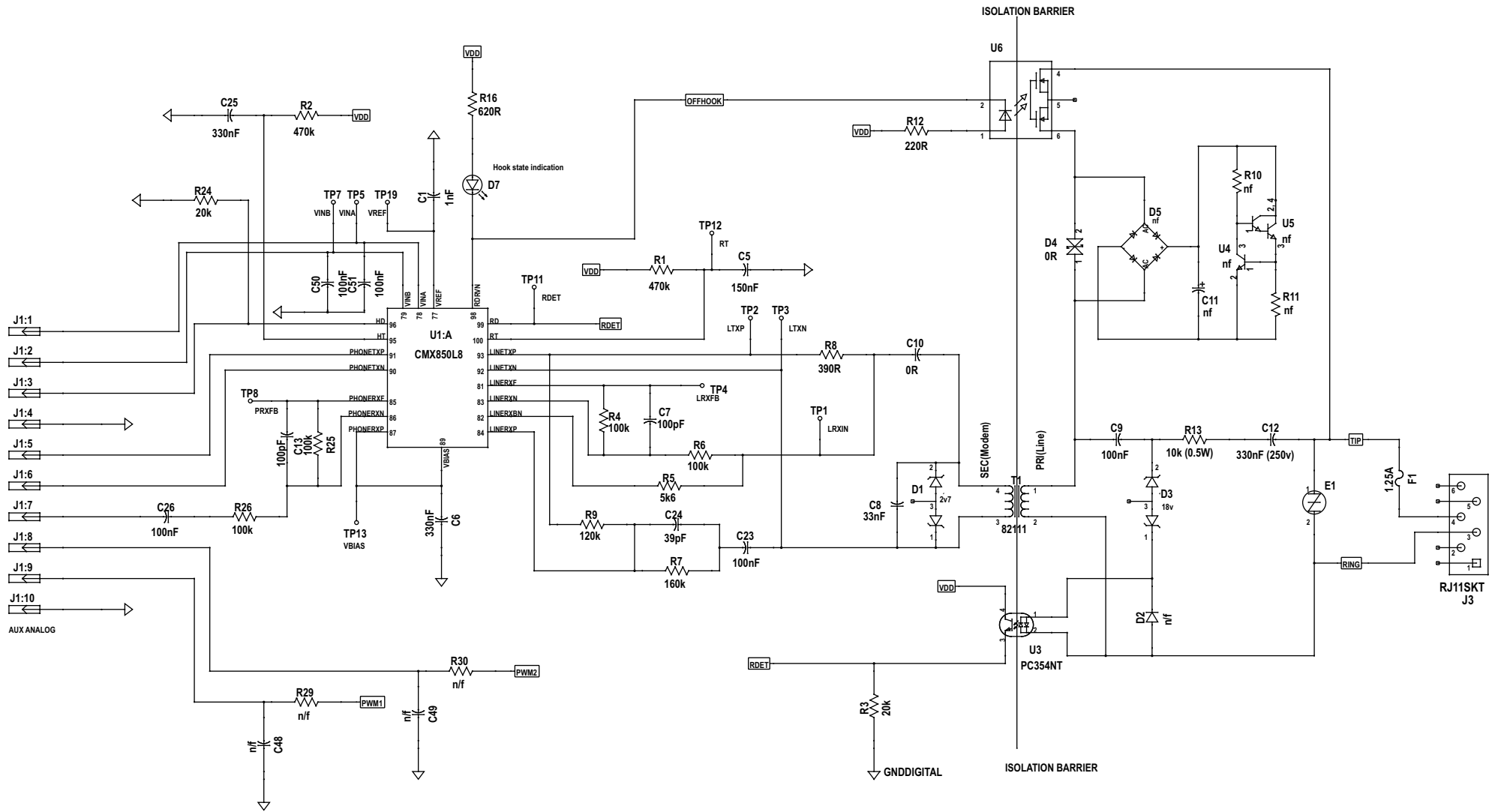




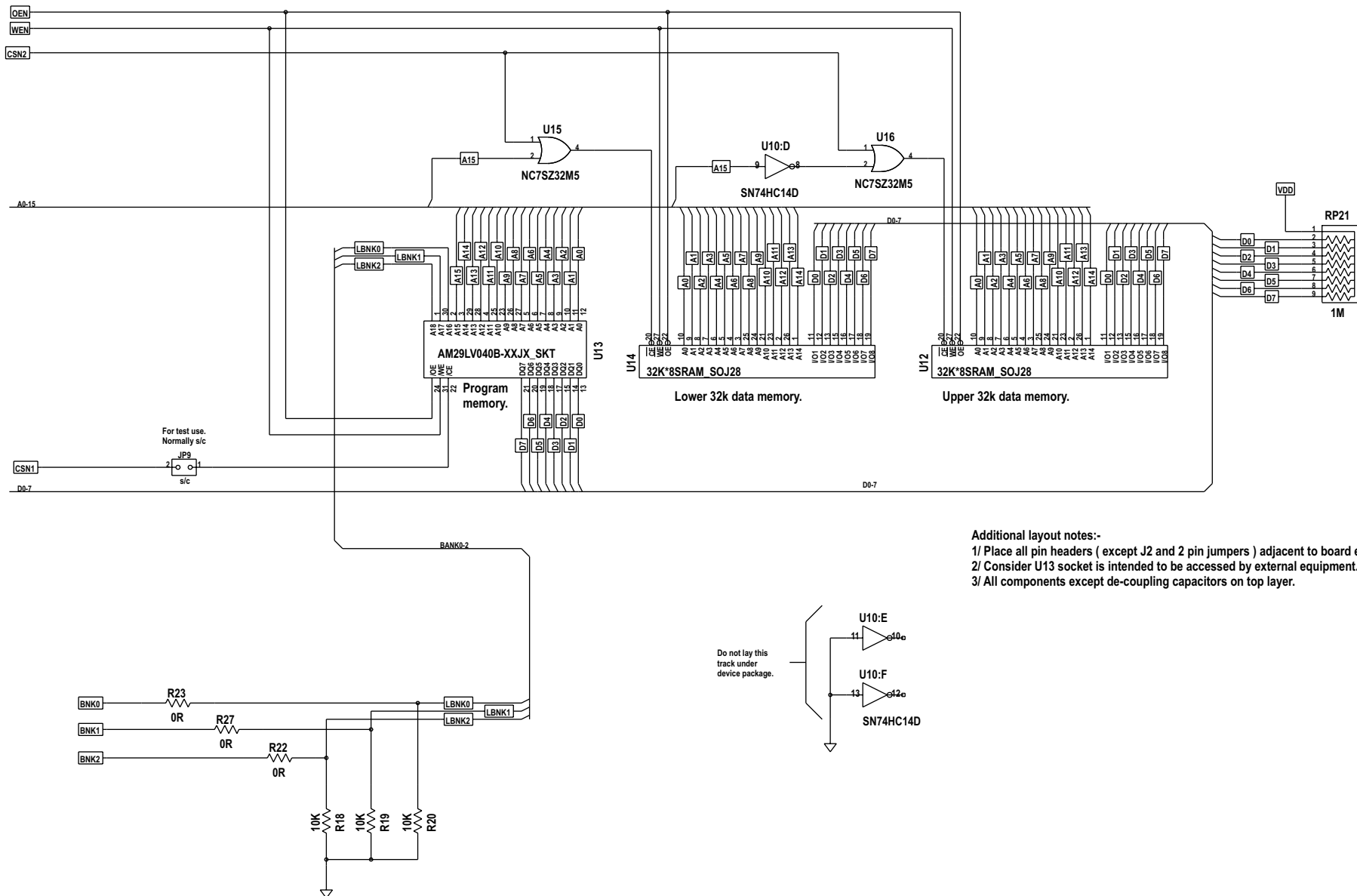
CML Microcircuits

COMMUNICATION SEMICONDUCTORS

High Resolution **Circuit Diagrams** for **EV8500**







Additional layout notes:-

- 1/ Place all pin headers (except J2 and 2 pin jumpers) adjacent to board edges.
- 2/ Consider U13 socket is intended to be accessed by external equipment.
- 3/ All components except de-coupling capacitors on top layer.

Do not lay this track under device package.

