

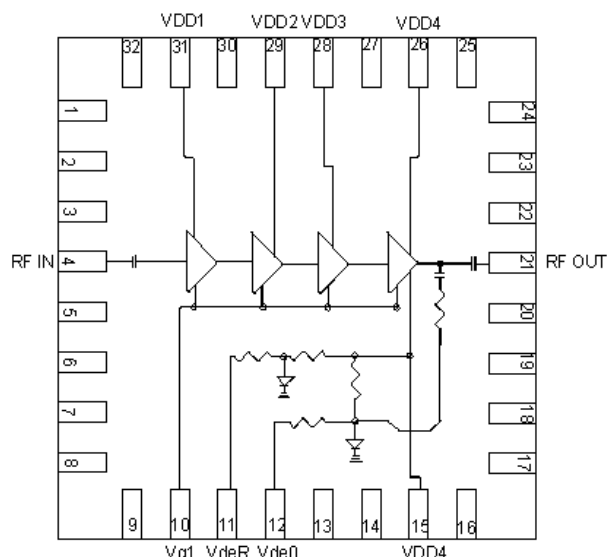
MMA-273336D-M5 27 - 33GHz, 4 W MMIC Power Amplifier

Features:

- Frequency Range: 27 – 33 GHz
- P1dB: +36 dBm
- IM3 Level: -38 dBc @Po = 20 dBm / tone
- Gain: 22 dB
- Vdd = 6 V
- Idsq = 1500 to 2800mA
- Input and Output Fully Matched to 50 Ω
- On-chip Output Power Detector

Applications:

- P2P Radio
- V-sat
- Military



Description:

The MMIC is a high power amplifier MMIC in a surface mount package designed for use in transmitters that operate at frequencies between 27 GHz and 33 GHz. In the operational frequency band, it provides 36 dBm of output power (P-1dB) and 22 dB of small-signal gain.

Absolute Maximum Ratings: (Ta = 25 °C)*

SYMBOL	PARAMETERS	UNITS	Min.	Max.
Vds	Drain-Source Voltage	V		6.5
Vg	Gate-Source Voltage	V	-2.1	0
Ig	First Gate Current	mA	-17	17
Pd	Power Dissipation	W		24
Pin max	RF Input Power	dBm		20
Tch	Channel Temperature	°C		+150
Tstg	Storage Temperature	°C		-55 to +150
Tmax	Max. Assembly Temp (20 sec max)	°C		+250

*Operation of this device above any one of these parameters may cause permanent damage.

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Electrical Specifications: $V_{ds} = 6\text{ V}$, $V_{gs} = -0.85\text{ V}$, $I_{dsq} = 2200\text{ mA}$, $T_a = 25^\circ\text{C}$, $Z_0 = 50\ \Omega$

Parameter	Units	Typical Data
Frequency Range	GHz	27 - 33
Gain (Typ / Min)	dB	22 / 20
Gain Flatness (Typ / Max)	+/- dB	3 / 4
Input RL (Typ / Max)	dB	10 / 8
Output RL (Typ / Max)	dB	10 / 8
VdeR	V	0.89
VdeO @29.5GHz, @ Po = +20dBm	V	0.85
@ Po = +35dBm	V	0.49
Output P1dB (Typ / Min)	dBm	35.5 / 35
Output P3dB (Typ / Min)	dBm	36.5 / 36
IM3 Level ⁽¹⁾	dBc	-36
Thermal Resistance	°C/W	3.8
Operating Current at P1dB (Typ / Max)	mA	2500 / 3000

(1) Output IP3 is measured with two tones at output power of 20 dBm / tone separated by 20 MHz.

ESD Caution

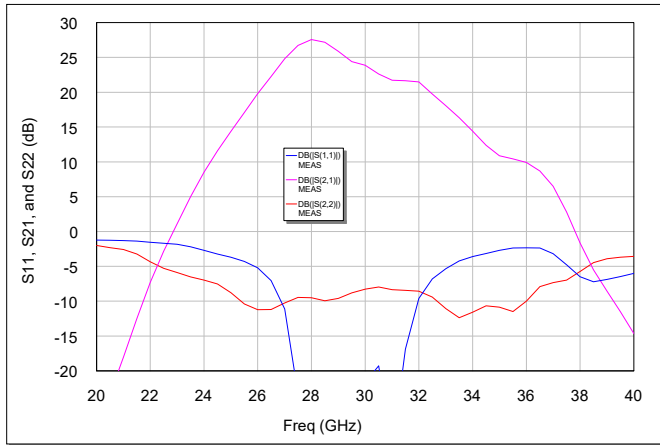


High-performance RF integrated circuits are highly sensitive to electrostatic discharge (ESD) and may contain unprotected inputs and outputs. Handling and assembly should be conducted in an ESD-controlled environment. It is recommended that ESD precautions and protocols are adopted to minimise the risk of electrostatic discharge and protect product integrity.

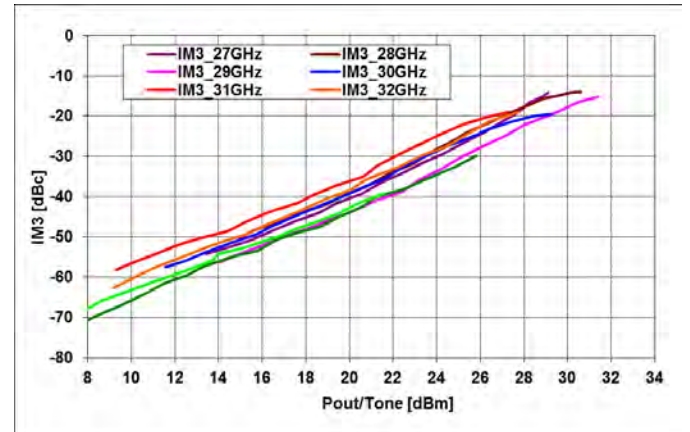
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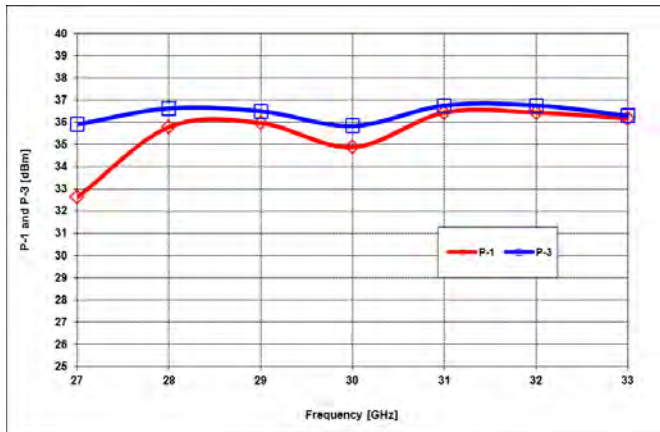
Typical RF Performance: $V_{ds} = 6\text{ V}$, $V_{gsq} = -0.85\text{ V}$, $I_{dsq} = 2200\text{ mA}$, $Z_0 = 50\text{ }\Omega$, $T_a = 25^\circ\text{C}$, $Z_0 = 50\text{ }\Omega$, $T_a = 25^\circ\text{C}$



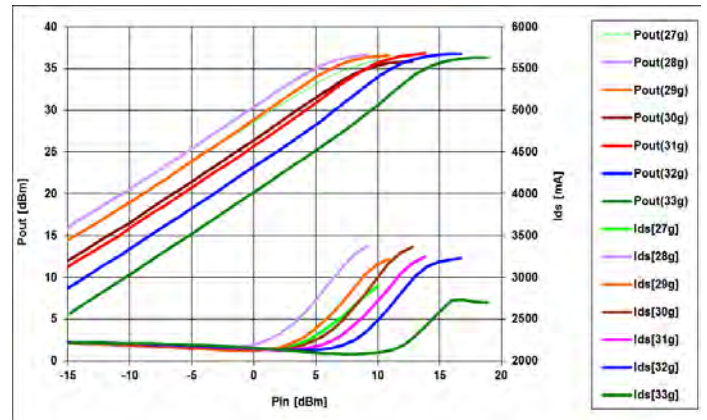
S11, S21, and S22 vs. Frequency



IM3 level [dBc] vs. Output power/tone [dBm]



P-1 and P-3 vs. Frequency

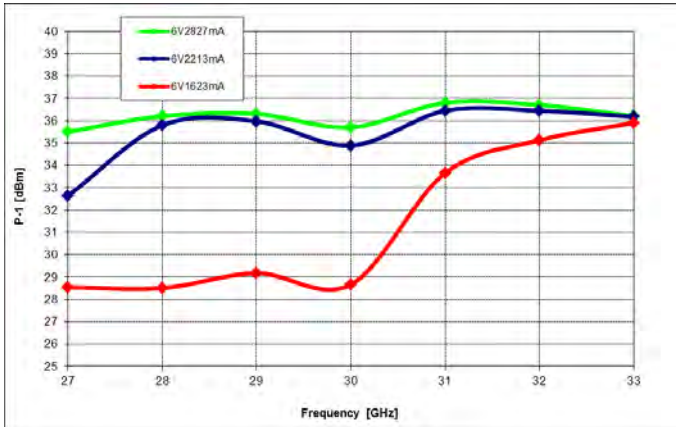


Po(dBm), and Ids(mA) vs. Pin(dBm)

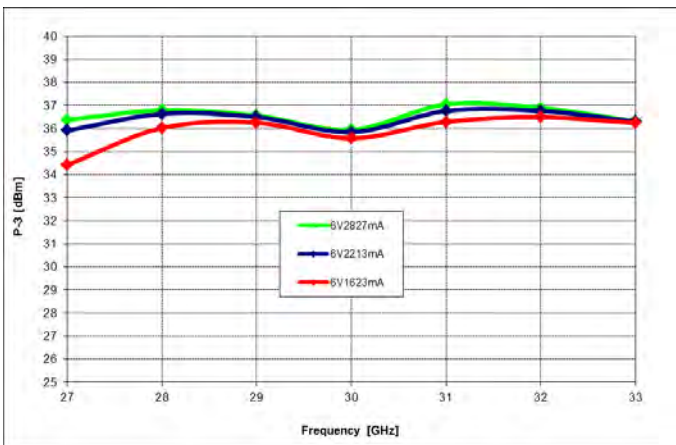
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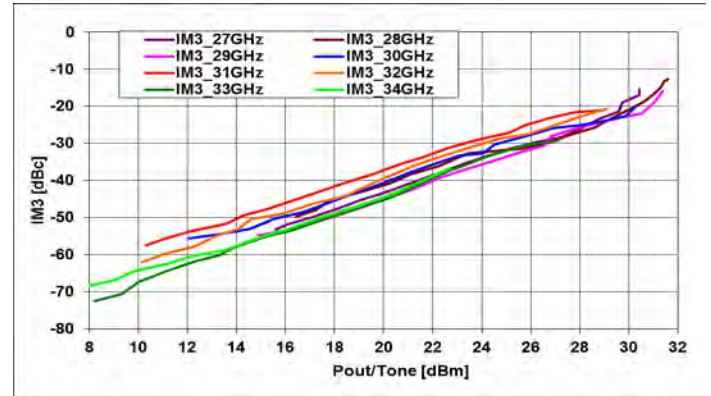
Typical Bias dependent RF Performance:



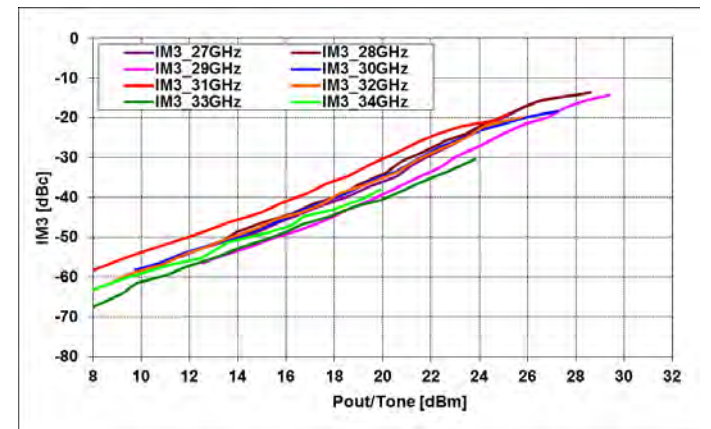
Bias dependent P1 vs. Frequency



Bias dependent P-3 vs. Frequency



IM3 Level [dBc] vs. output power/tone [dBm]
@Vds=6V, Idsq=2.8A

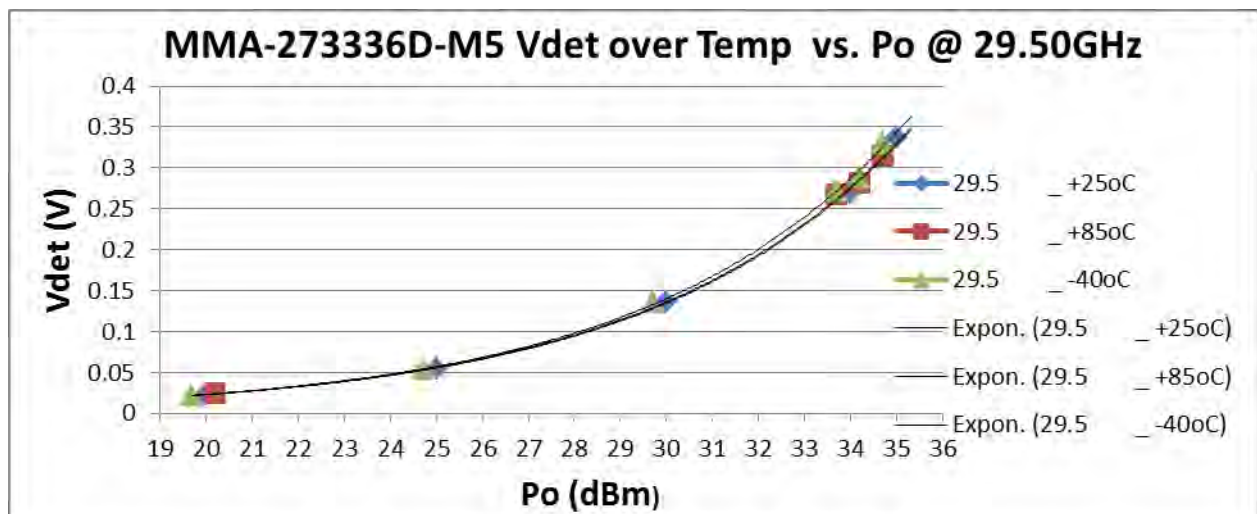
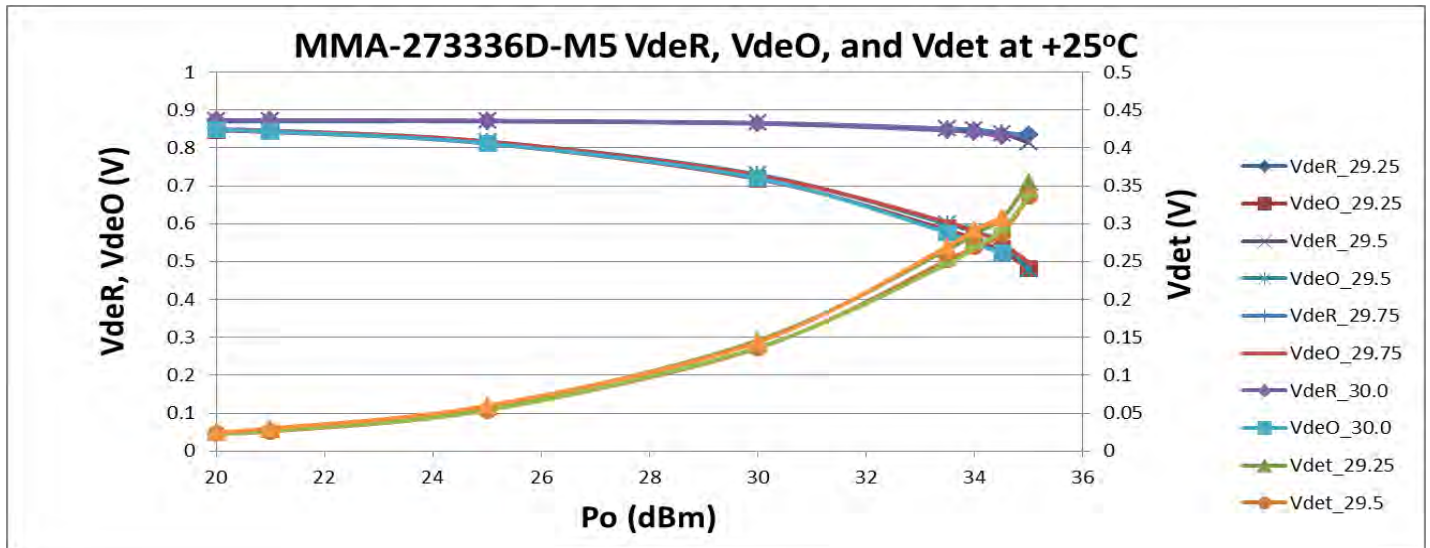


Pout[dBm], and Idsq[mA] vs. Input power [dBm]
@Vds=6V, Idsq=1.5A

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Typical Vdet (= VdeR - VdeO) Performance over Temperature



Applications

The **MMA-273336D-M5** MMIC power amplifier is designed for use as a power stage amplifier in microwave transmitters. It is ideally suited for 27 to 33 GHz band V-sat transmitter applications requiring excellent saturated output power performance. This amplifier is provided as a 5 x 5 mm QFN package, and the packaged amplifier is fully compatible with industry standard high volume surface mount PCB assembly processes.

Biasing and Operation

The recommended bias conditions for best performance for the **MMA-273336D-M5** are $V_{DD} = 6.0\text{ V}$, $I_{dsq} = 2200\text{ mA}$. Performance improvements are possible depending on applications. The drain bias voltage range is 5 to 6 V and the quiescent drain current biasing range is 1500 mA to 2800 mA. A single DC gate supply connected to V_g will bias all the amplifier stages. Muting can be accomplished by setting V_g to the pinch-off voltage ($V_p = -2\text{ V}$). The gate voltage (V_g) should be applied prior to the drain voltages (V_{d1} , V_{d2} , V_{d3}) during power up and removed after the drain voltages during power down. The RF input and output ports are DC decoupled internally. Typical DC supply connection with bi-passing capacitors for the **MMA-273336D-M5** is shown in following pages.

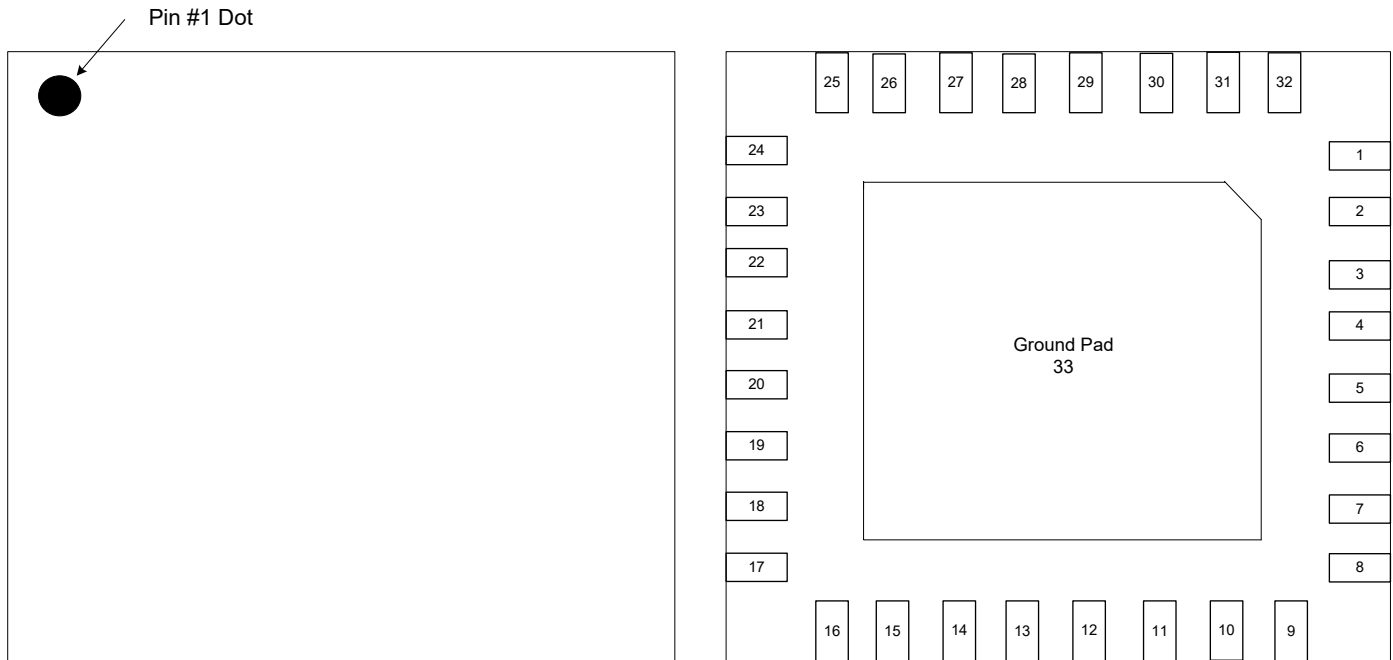
Assembly Techniques

GaAs MMICs are ESD sensitive. ESD preventive measures must be employed in all aspects of storage, handling, and assembly. MMIC ESD precautions, handling considerations, die attach and bonding methods are critical factors in successful GaAs MMIC performance and reliability.

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Package Pin-out:

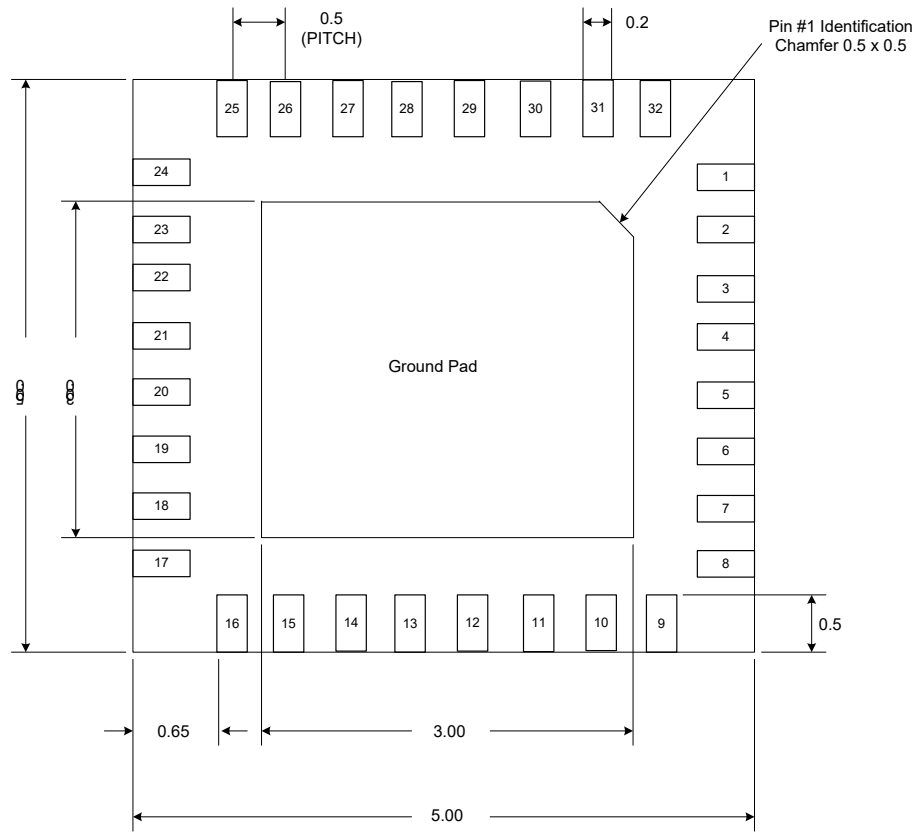


Pin	Description
4	RF Input
21	RF Output
10	Vg
31	Vd1
29	Vd2
28	Vd3
15, 26	Vd4
11	VdeR
12	VdeO
1, 3, 5, 8, 9, 16, 17, 20, 22, 24, 25, 32, 33	Ground
2, 6, 7, 11, 12, 13, 14, 18, 19, 23, 27, 30	N/C

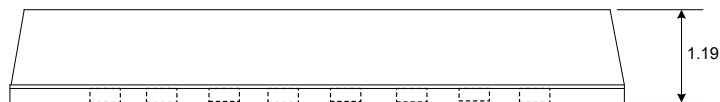
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Mechanical Information:



BOTTOM VIEW



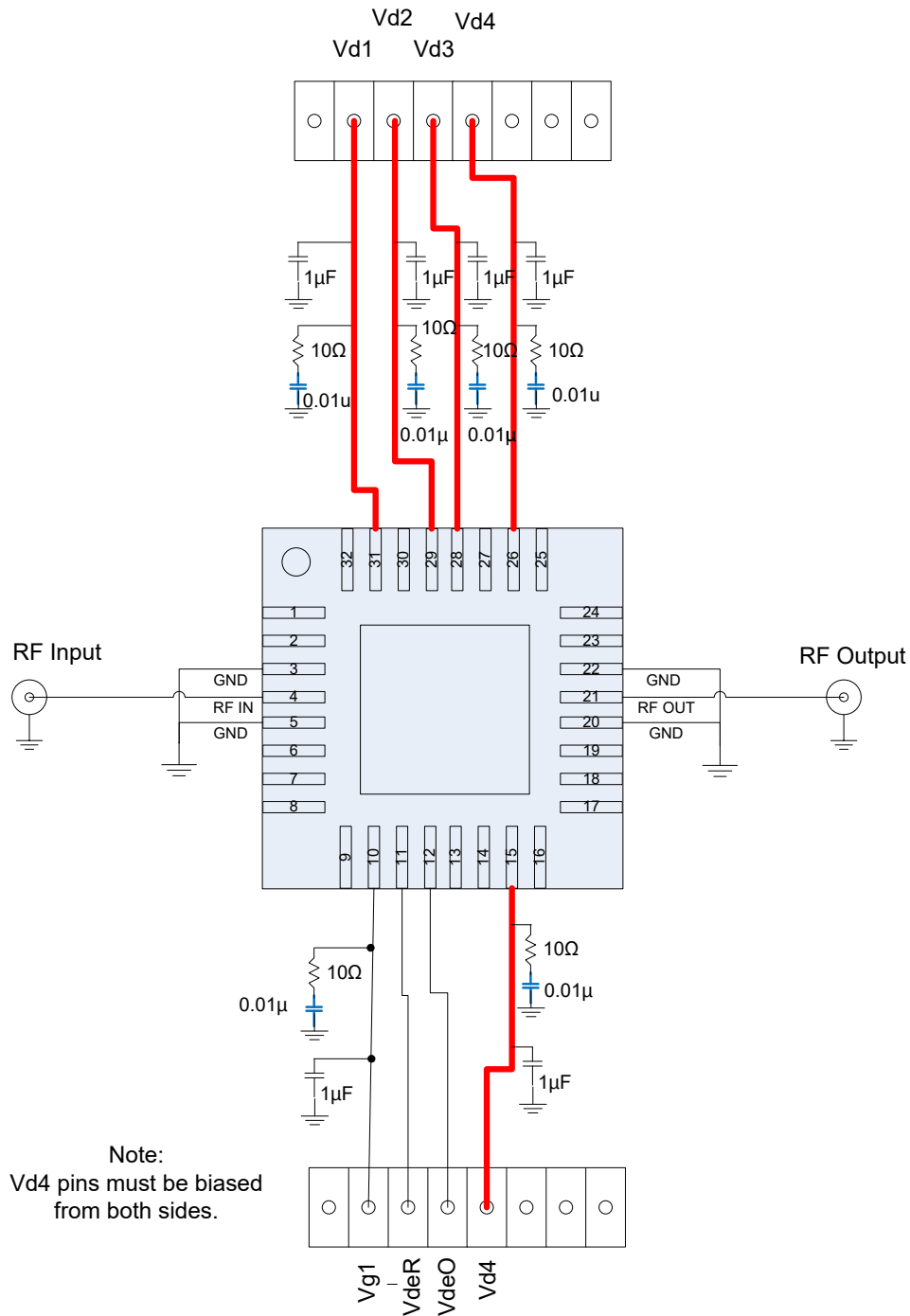
SIDE VIEW

The units are in [mm].

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Application Circuit:



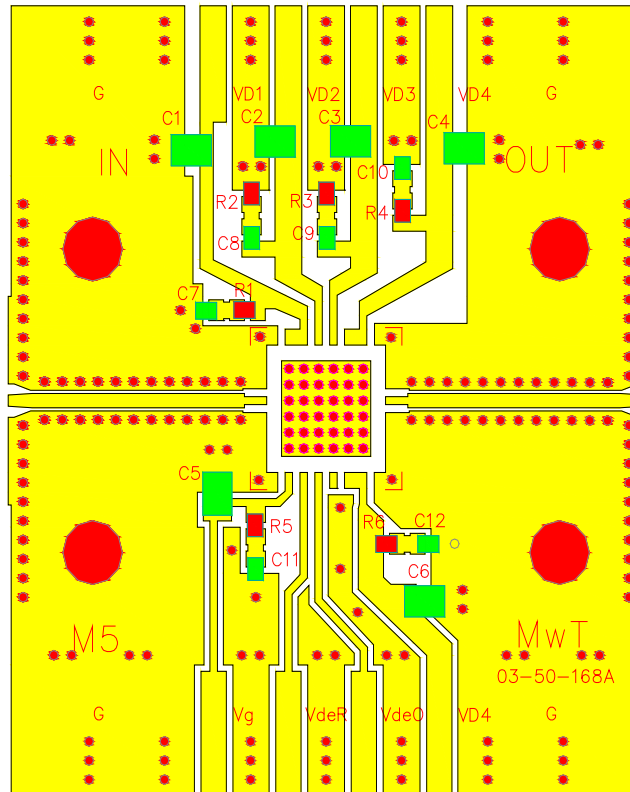
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Recommended Application Board Design:

Board Material is 10 mil (Dielectric) thickness Rogers 4350B with 0.5 oz copper clads.

Board is soldered on a gold plated solid copper block and adequate heat-sinking is required for 16.8 W total power dissipation.



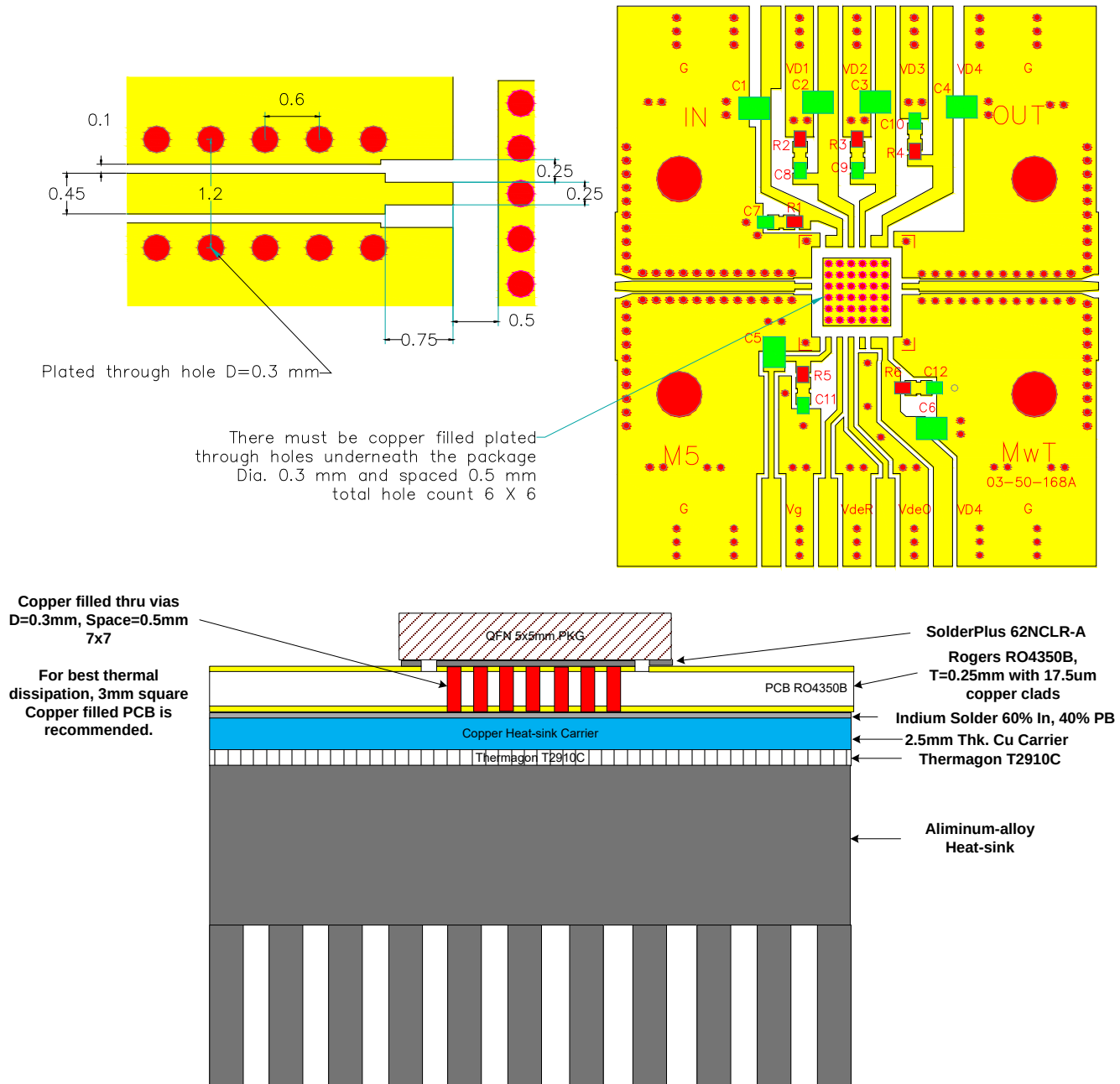
Part	Description
C1, C2, C3, C4, C5, C6	1 μ F Capacitor (0603)
C7, C8, C9, C10, C11, C12	0.01 μ F Capacitor (0402)
R1, R2, R3, R4, R5, R6	10 Ω Resistor (0402)

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Recommended Application Board Design:

Board Material is 10 mil (Dielectric) thickness Rogers 4350B with 0.5 oz copper clads. The board material and mounting pattern, as defined in the data sheet, optimizes RF performance and is strongly recommended.



RoHS Compliance



CML Micro certifies, to the best of our knowledge, that its products comply with RoHS directive (2011/65/EU), as amended by Directive (EU) 2015/863.

REACH Compliance



CML Micro certifies, to the best of our knowledge, that its products do not contain any of the banned substances specified in the SVHC list at <https://echa.europa.eu/candidate-list-table>. CML Micro are committed to ensuring the safety of products and the compliance with regulations. Publications will be updated in the event that REACH control is compromised.

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