

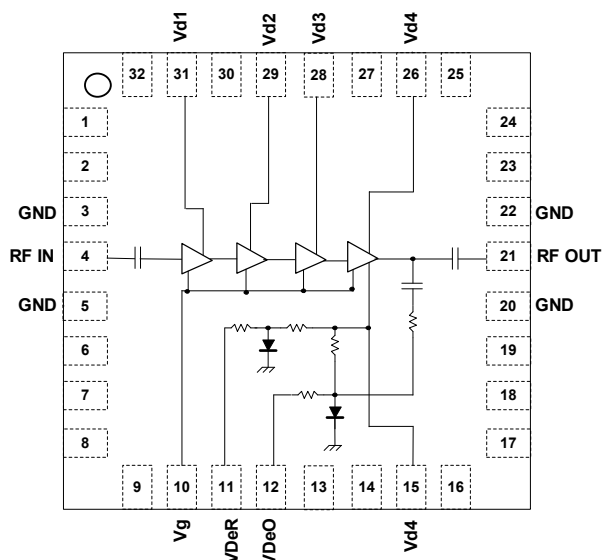
MMA-243034D-M5 24 – 30 GHz, 2.5 W MMIC Power Amplifier

Features:

- Frequency Range: 24 – 30 GHz
- P1dB: 34 dBm
- IM3 Level: -25 dBc @ Po = 27 dBm / tone
- Gain: 23 dB
- Vdd = 6 V
- Idsq = 1100 to 1800 mA
- Input and Output Fully Matched to 50 Ω
- Output Power Detector

Applications:

- Point-to-Point Radio
- VSAT
- 5G



Functional Block Diagram

Description:

The MMA-243034D-M5 is a 2.5 W GaAs pHEMT MMIC power amplifier in a compact 5 mm QFN surface mount package. The MMA-243034D-M5 provides 34 dBm of output power (P-1dB) and 23 dB of small-signal gain from 24 GHz to 30 GHz.

Absolute Maximum Ratings: (Ta = 25 °C)*

SYMBOL	PARAMETERS	UNITS	Min.	Max.
Vd	Drain Voltage	V		6.5
Vg	Gate Voltage	V	-2.1	0
Ig	Gate Current	mA	-17	17
Pd	Power Dissipation	W		24
Pin max	RF Input Power	dBm		20
Tch	Channel Temperature	°C		+150
Tstg	Storage Temperature	°C		-55 to +150
Tmax	Max. Assembly Temp (20 sec max)	°C		+250

*Operation of this device above any one of these parameters may cause permanent damage.

MMA-243034D-M5

24 – 30 GHz, 2.5 W MMIC Power Amplifier

Electrical Specifications: $V_d = 6\text{ V}$, $I_{dq} = 1600\text{ mA}$, $T_a = 25\text{ }^{\circ}\text{C}$, $Z_o = 50\text{ }\Omega$

Parameter	Units	Typical Data
Frequency Range	GHz	24 - 30
Gain	dB	23
Gain Flatness	$\pm$ dB	2
Input Return Loss	dB	8
Output Return Loss	dB	10
VdeR	V	0.9
VdeO @28GHz, @ Po = +20 dBm	V	0.82
@ Po = +33 dBm	V	0.0
Output P1dB	dBm	34
Output P3dB	dBm	34.5
IM3 Level ⁽¹⁾	dBc	-25
Thermal Resistance	$^{\circ}\text{C}/\text{W}$	5.3
Total Drain Current at P1dB	mA	2100

(1) Output IP3 is measured with two tones at output power of 27 dBm / tone separated by 20 MHz.

ESD Caution



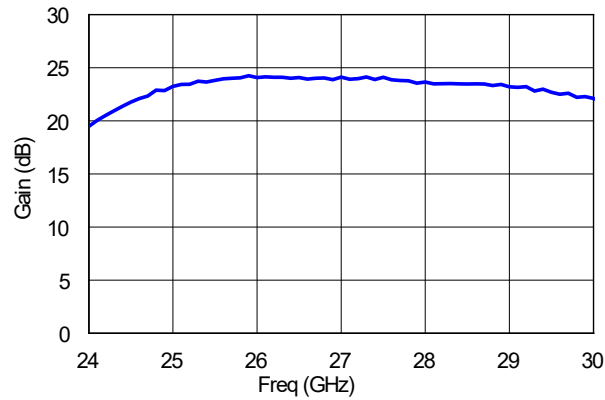
High-performance RF integrated circuits are highly sensitive to electrostatic discharge (ESD) and may contain unprotected inputs and outputs. Handling and assembly should be conducted in an ESD-controlled environment. It is recommended that ESD precautions and protocols are adopted to minimise the risk of electrostatic discharge and protect product integrity.

MMA-243034D-M5

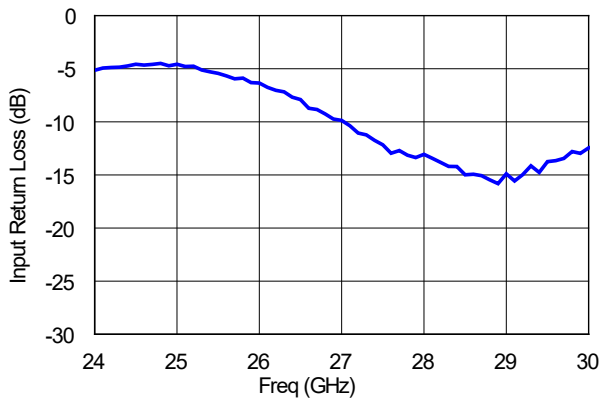
24 – 30 GHz, 2.5 W MMIC Power Amplifier

Typical RF Performance: $V_d = 6\text{ V}$, $I_{dQ} = 1600\text{ mA}$, $V_g = -0.85\text{ V}$ typical, $Z_o = 50\ \Omega$

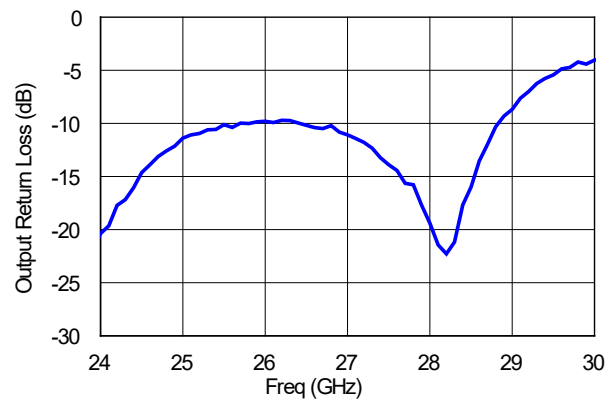
Small Signal Gain vs. Frequency



Input Return Loss vs. Frequency



Output Return Loss vs. Frequency

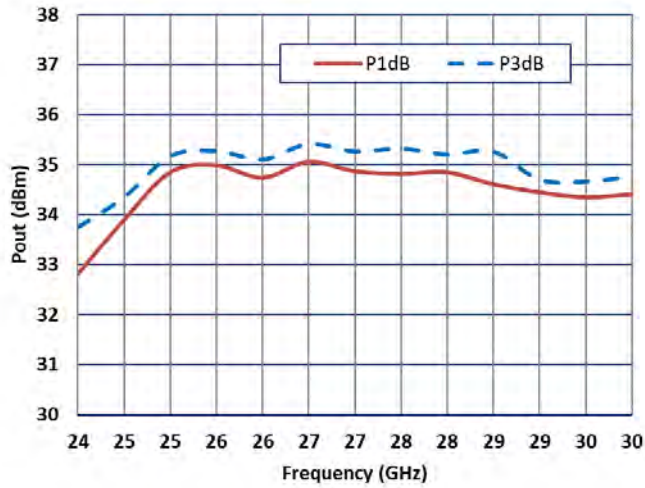


MMA-243034D-M5

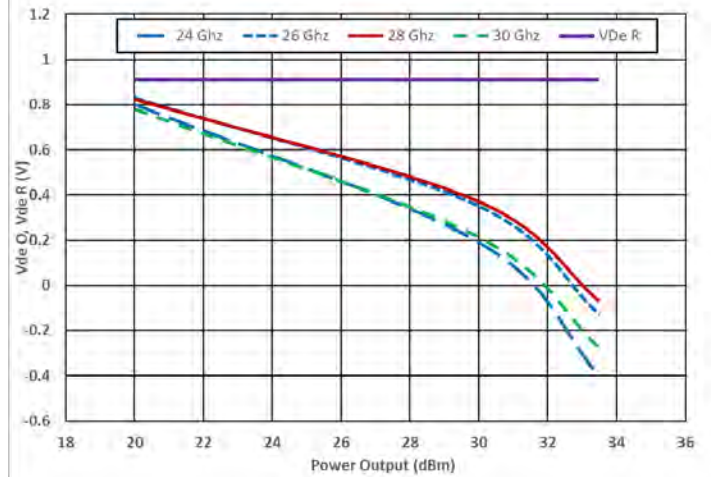
24 – 30 GHz, 2.5 W MMIC Power Amplifier

Typical RF Performance (Cont): $V_d = 6\text{ V}$, $I_{dq} = 1600\text{ mA}$, $V_g = -0.85\text{ V typical}$, $Z_o = 50\ \Omega$

Output Power



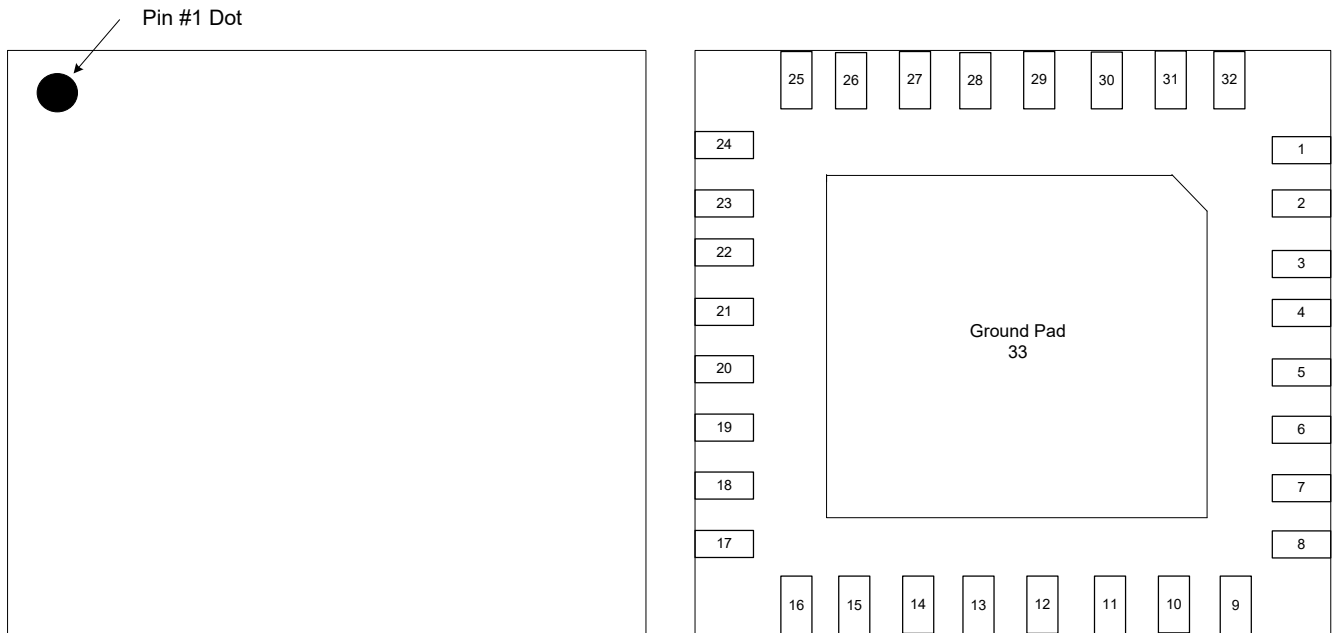
Detector Output vs. Output Power



MMA-243034D-M5

24 – 30 GHz, 2.5 W MMIC Power Amplifier

Package Pin Designations:



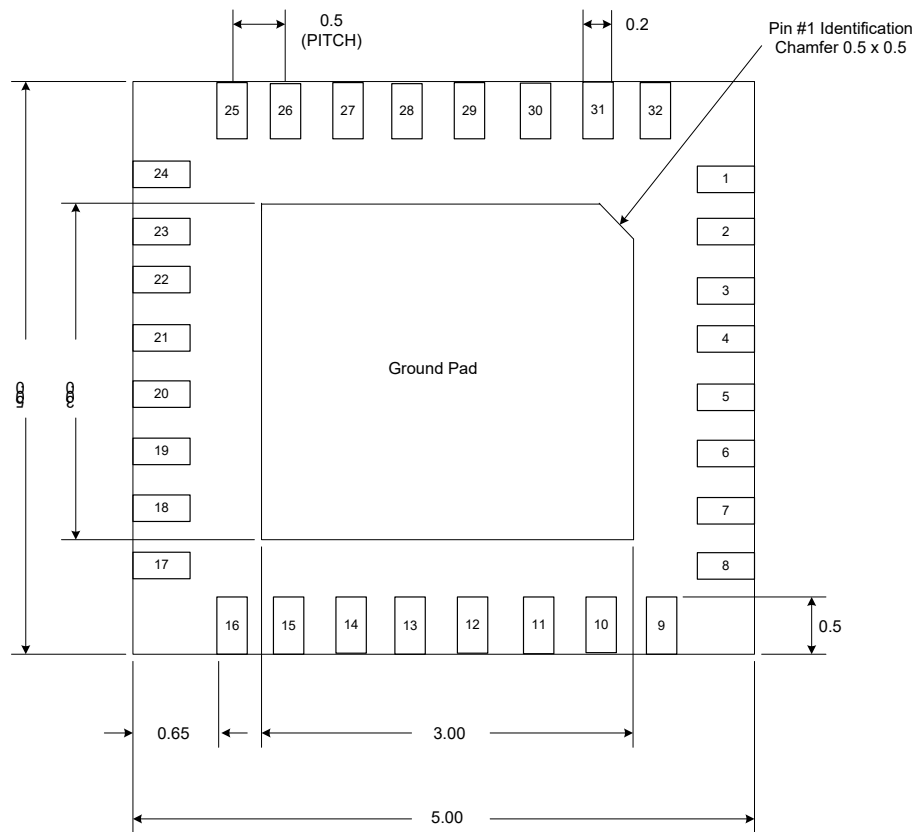
Pin	Description
4	RF Input
21	RF Output
10	Vg
31	Vd1
29	Vd2
28	Vd3
15, 26	Vd4
11	VdeR
12	VdeO
1, 3, 5, 8, 9, 16, 17, 20, 22, 24, 25, 32, 33	Ground
2, 6, 7, 11, 12, 13, 14, 18, 19, 23, 27, 30	N/C

MMA-243034D-M5

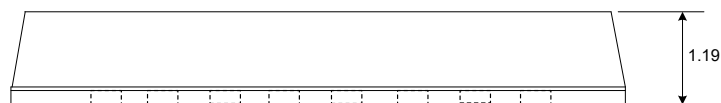
24 – 30 GHz, 2.5 W MMIC Power Amplifier

Mechanical Drawing:

The package is a 32-Lead 5 x 5 mm air-cavity QFN package that is compatible with industry standard surface mount PCB assembly processes.



BOTTOM VIEW



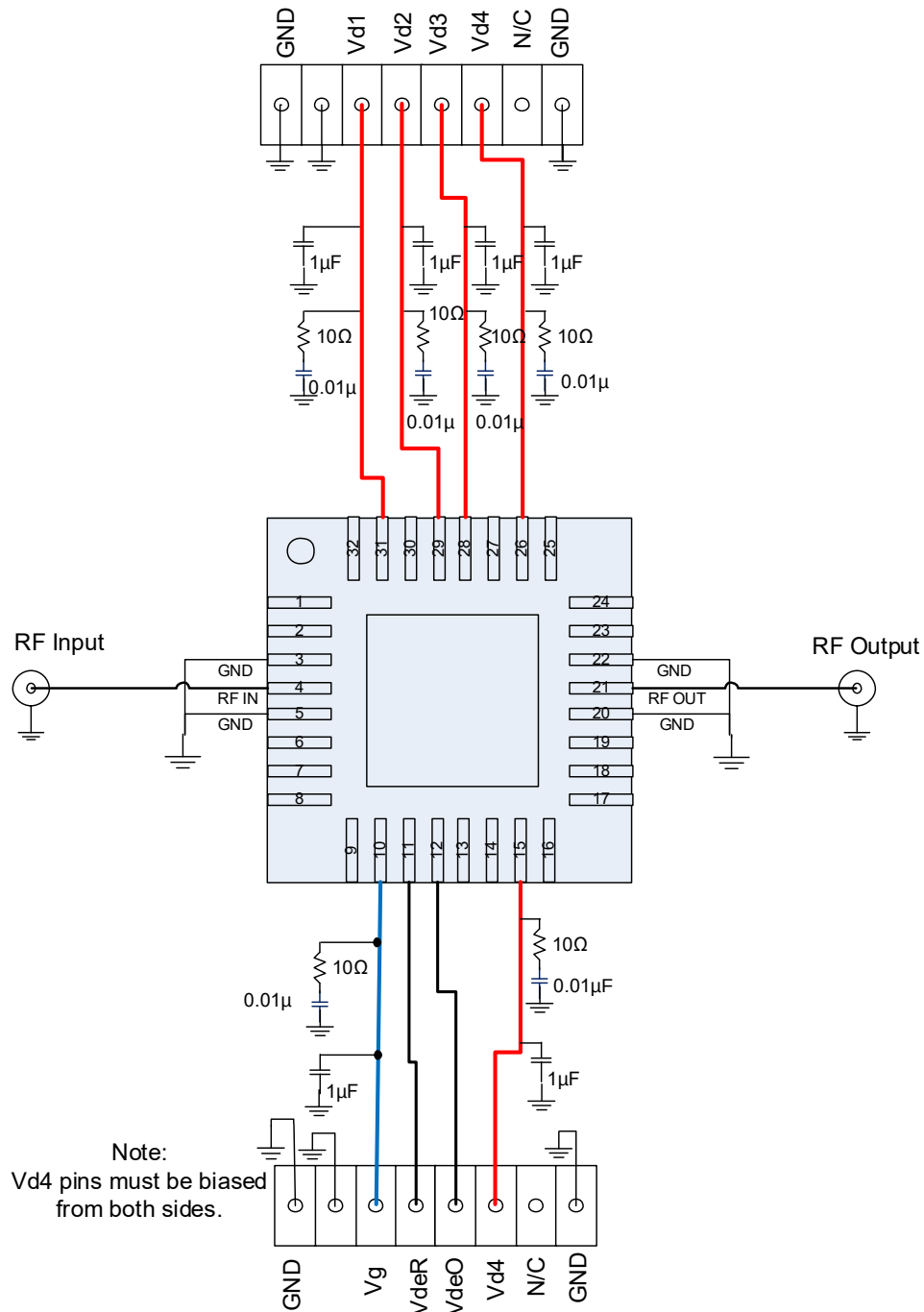
SIDE VIEW

The units are in [mm].

MMA-243034D-M5

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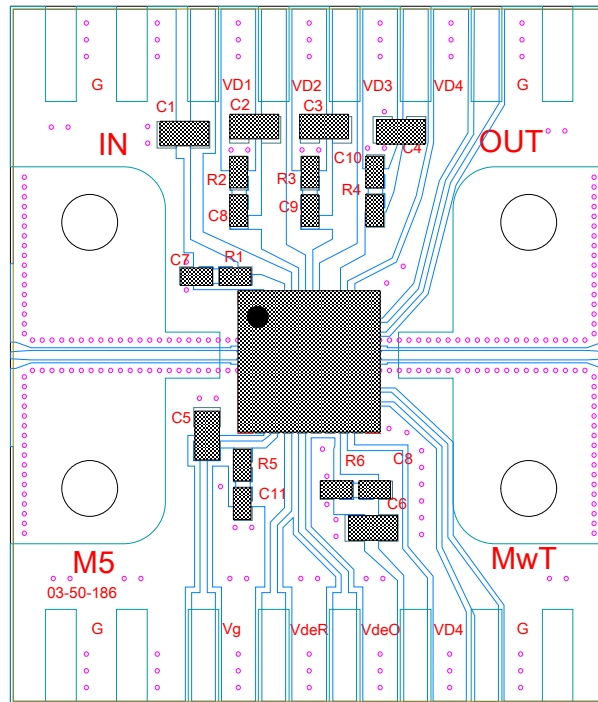
Sample Application Circuit:



MMA-243034D-M5

24 – 30 GHz, 2.5 W MMIC Power Amplifier

Sample Application Board Design:



Part	Description
C1, C2, C3, C4, C5, C6	1 μ F Capacitor (0603)
C7, C8, C9, C10, C11, C12	0.01 μ F Capacitor (0402)
R1, R2, R3, R4, R5, R6	10 Ω Resistor (0402)
Board Material	Rogers RO4350B, 10 Mil Dielectric Thickness ½ oz. Copper Cladding, Copper-Filled Via Holes

MMA-243034D-M5

24 – 30 GHz, 2.5 W MMIC Power Amplifier

Biassing and Operation

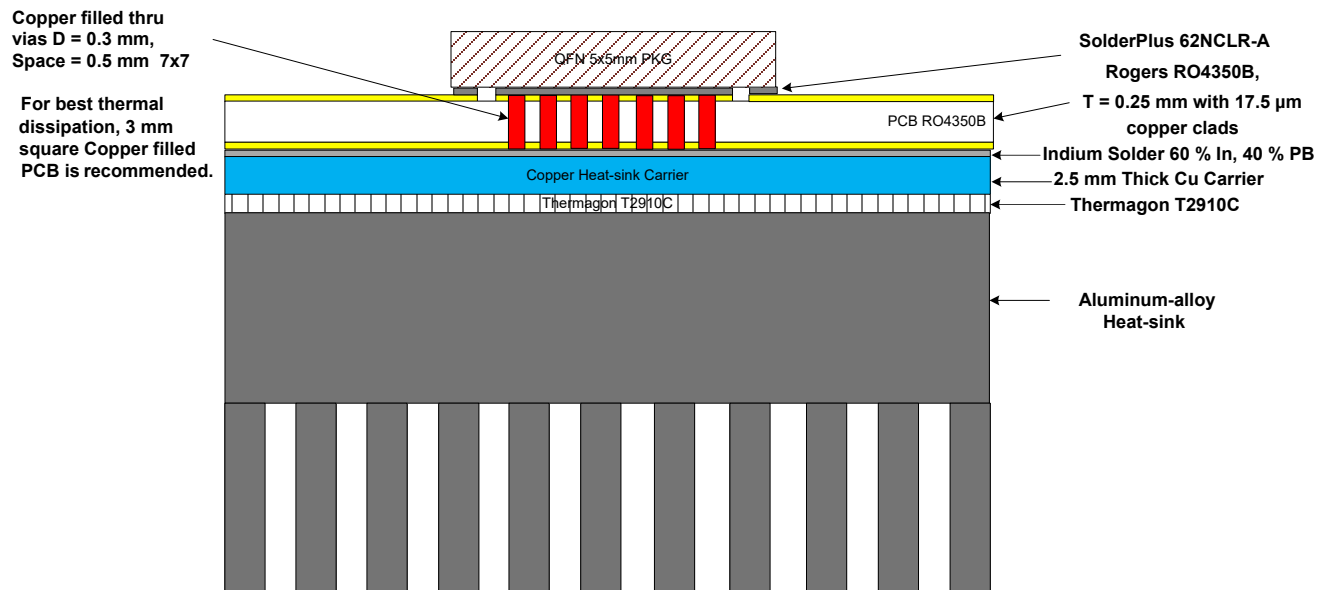
The recommended bias conditions for optimum performance for the **MMA-243034D-M5** are $V_{DD} = 6.0\text{ V}$, $I_{dq} = 1600\text{ mA}$. The gate voltage (V_g) must be applied prior to the drain voltages (V_{d1} , V_{d2} , V_{d3} , V_{d4}) during power up and removed after the drain voltages during power down. A single DC gate supply connected to V_g will bias all the amplifier stages. Muting can be accomplished by setting V_g to the pinch-off voltage ($V_p = -2\text{ V}$). V_{d4} must be connected to both V_{d4} pins.

Assembly and Handling

GaAs MMICs are ESD sensitive. ESD preventive measures must be employed in all aspects of storage, handling, and assembly.

Sample Application Board Design:

Proper heatsinking and board mounting pattern with filled thermal vias are recommended for optimum performance.



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RoHS Compliance



CML Micro certifies, to the best of our knowledge, that its products comply with RoHS directive (2011/65/EU), as amended by Directive (EU) 2015/863.

REACH Compliance



CML Micro certifies, to the best of our knowledge, that its products do not contain any of the banned substances specified in the SVHC list at <https://echa.europa.eu/candidate-list-table>. CML Micro are committed to ensuring the safety of products and the compliance with regulations. Publications will be updated in the event that REACH control is compromised.

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