

Senior Analogue IC Design Engineer

CML Vision – The first-choice semiconductor partner to technology innovators, together transforming how the world communicates.

CML Values – Trust, Respect, Commitment and Creativity.

CML Guiding Principles – Strong business ethics; culture of quality with a sense of urgency; live and breathe the customer experience; a passion for excellence; inspire our people to innovate.

Position Summary

A hands-on role for a self-motivated Analogue IC Design engineer who will join our New Product Engineering team working on the design of Analogue and Mixed signal circuits at our design centre in Maldon, Essex, United Kingdom. Your responsibilities include circuit design, implementation and verification of complete sub-systems for sub-6G ICs.

Experience in some or all of the following is required: strong understanding of basic concepts and theories in Analog and RF design, such as mismatch, linearity, stability and noise. The design of Power Management circuits, PTAT, Bandgap, VGA, Filter, TIA, PLL, or other baseband modules in BiCMOS and/or RF-CMOS processes.

Collaborating with the test team across multi sites, you will also contribute to the development of the test plan and ASIC characterization.

The person appointed to the role will report to the Head of Silicon Engineering.

Essential Functions / Key Focus Areas

- Designing blocks such as filters, high speed multiplexer, divider circuits, bias circuits, bandgap, fractional N PLL, etc
- Working with a variety of technologies including SiGe & CMOS to implement innovative high-performance IC
- Excellent time management and organisation skills
- Produce detailed design documentation of all blocks developed
- Support product evaluation
- Open collaborative mind-set
- Good communicator

Qualifications and Experience

- Master's degree or PhD in a closely related field; or proven industrial track record
- A minimum of five years' experience of IC design
- Expertise with Cadence Virtuoso tools and version control
- Silicon experience of SiGe Bipolar and CMOS technologies
- Successful track record of right first-time silicon
- An understanding of design for manufacture
- Working knowledge of IC layout techniques in a Cadence Virtuoso environment
- Familiarisation of ESD protection techniques

This job description is not exhaustive and is subject to change from time to time to meet the needs of the business

Critical Competencies for Success

Excellent Communication Skills – Maintains an open flow of two-way communication, exhibiting strong verbal, written and presentation skills, stating expectations clearly and being visible to all stakeholders. Exhibits executive presence in front of both internal and external audiences. Being a good listener is a critical success factor.

Impeccable Integrity – Embodies and practices unquestioned personal and business integrity. Creates an environment of trust and respect by acting with fairness and consistency, keeping commitments, and providing rationale for decisions.

Team Player with Maturity – A results-oriented individual who, at the same time, is the consummate team player who can effectively negotiate with others to achieve win-win outcomes. They will be well respected by senior management, peers, and subordinates, and will be secure in their technical and managerial abilities.

Open Minded – A resilient and committed executive with a passion for excellence; a flexible and open operating style; non-political and non-bureaucratic; someone with a natural and effective consultative approach who will gain the trust and respect of individuals at all levels.

Customer Focused – Live and breathe the customer experience by encouraging a culture dedicated to enhancing customer satisfaction and building strong customer relationships.

Location

The position is hybrid and offices are based at our headquarters in Langford, near Maldon, Essex, and offers excellent working conditions on a 29-acre parkland site. Due to rural location, own transport is essential.

Salary and Benefits:

A competitive salary is offered, depending upon skills and experience.

Additional benefits include:

- Holidays – 25 days plus bank holidays per year
- Pension scheme
- Healthcare scheme
- Hybrid working
- Death in service (4 times salary)
- On-site parking
- VISA sponsorship (if applicable)
- Relocation package (if applicable)

Application

In the first instance, please forward your CV to Dr. Maryam Shaeri at mshaeri@cmlmicro.com.

Further information about CML Microcircuits can be found on our website at www.cmlmicro.com